

S/N 09/945,500

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Leonard Forbes

Examiner: Ly Pham

Serial No.: 09/945,500

Group Art Unit: 2827

Filed: August 30, 2001

Docket: 1303.029US1

Title: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH  
LOW TUNNEL BARRIER INTERPOLY INSULATORS



**RESPONSE TO NOTICE OF DRAWING INCONSISTENCY**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Notice of Drawing Inconsistency with Specification mailed on October 14, 2005, please amend the above-identified application as follows: